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Method : 8082A

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Data For Sequence PO090320 CHEMTECH-NJ Lab Sequence Rule For Ms/Msd of EPH

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No Review & No Supervised

Reviewed & No Supervised

Reviewed & Supervised

SampleID	Data File	Date & Time	ClientId	Test	QCType	Dilution.	Comments	Status	System Comments
HEXANE	PO071132.D	03 Sep 2020 8:55	HEXANE	PCB	HEXANE	1	--	OK	UPDATE STATUS
I.BLK	PO071133.D	03 Sep 2020 9:12	I.BLK	PCB	IBLK	1	--	OK	UPDATE STATUS
AR1660CCC500	PO071134.D	03 Sep 2020 9:29	AR1660CCC500	PCB	CCAL500	1	--	OK	UPDATE STATUS
L3876-08	PO071135.D	03 Sep 2020 10:06	1495-96	PCB	SAM	2	AR1260 hits, DCB failing high in both column	RERUN	Surr Fail UPDATE STATUS
PB131358BL	PO071136.D	03 Sep 2020 10:23	PB131358BL	PCB	MB	1	--	OK	UPDATE STATUS
L3875-01RE	PO071137.D	03 Sep 2020 10:40	DIELETRIC-FLUID-TOTE-F7RE	PCB	SAM	1	TCMX failing high in both column	CONFIRMS	Surr Fail UPDATE STATUS
L3876-05	PO071138.D	03 Sep 2020 10:57	1577	PCB	SAM	1	--	OK	UPDATE STATUS
L3876-08RE	PO071139.D	03 Sep 2020 11:53	1495-96	PCB	SAM	2	AR1260 hits, DCB failing high in both column	CONFIRMS	Surr Fail UPDATE STATUS
PB131393BL	PO071140.D	03 Sep 2020 12:12	PB131393BL	PCB	MB	1	--	OK	UPDATE STATUS
PB131393BS	PO071141.D	03 Sep 2020 12:29	PB131393BS	PCB	LCS	1	--	OK	UPDATE STATUS
		03							

L3871-01	PO071142.D	Sep 2020 12:46	TR-05-090220	PCB	SAM	1	--	OK	UPDATE STATUS
L3877-02	PO071143.D	03 Sep 2020 13:03	TP-1	PCB	SAM	1	TCMX failing high in both column & TCMX having F flag in both column	NotOK	Surr Fail UPDATE STATUS
L3877-07	PO071144.D	03 Sep 2020 13:20	TP-2	PCB	SAM	1	--	OK	UPDATE STATUS
L3877-12	PO071145.D	03 Sep 2020 13:37	TP-3	PCB	SAM	1	--	OK	UPDATE STATUS
L3877-17	PO071146.D	03 Sep 2020 13:55	TP-4	PCB	SAM	1	TCMX failing high in 2nd column	OK	Surr Fail UPDATE STATUS
L3888-02	PO071147.D	03 Sep 2020 14:12	9-2-TP-1	PCB	SAM	1	DCB failing low in both column	RERUN	Surr Fail UPDATE STATUS
L3888-02MS	PO071148.D	03 Sep 2020 14:29	9-2-TP-1MS	PCB	MS	1	--	OK	Surr Fail UPDATE STATUS
L3888-02MSD	PO071149.D	03 Sep 2020 14:46	9-2-TP-1MSD	PCB	MSD	1	Recovery fail for AR1016 compound	OK	Surr, Recovery Fail UPDATE STATUS
I.BLK	PO071150.D	03 Sep 2020 15:03	I.BLK	PCB	IBLK	1	--	OK	UPDATE STATUS
AR1660CCC500	PO071151.D	03 Sep 2020 15:38	AR1660CCC500	PCB	CCAL500	1	--	OK	UPDATE STATUS
L3877-02	PO071152.D	03 Sep 2020 15:55	TP-1	PCB	SAM	1	TCMX failing high in both column & TCMX having F flag in both column, Need cleanup	NotOK	Surr Fail UPDATE STATUS
L3877-17	PO071153.D	03 Sep 2020 16:12	TP-4	PCB	SAM	1	AR1242 hits, Need 2X, already run maybe TYPO	NotOK	OR Fail UPDATE STATUS
L3888-06	PO071154.D	03 Sep 2020 16:29	9-2-TP-2	PCB	SAM	1	--	OK	UPDATE STATUS
L3888-10	PO071155.D	03 Sep 2020 16:46	9-2-TP-3	PCB	SAM	1	--	OK	UPDATE STATUS
L3888-14	PO071156.D	03 Sep 2020 17:03	9-2-TP-4	PCB	SAM	1	--	OK	UPDATE STATUS
L3888-02	PO071157.D	03 Sep 2020 17:20	9-2-TP-1	PCB	SAM	1	AR1260 hits,DCB low in 1st column	OK	Surr Fail UPDATE STATUS
L3895-09RE	PO071158.D	03 Sep 2020	SB-23A-(7-7.5) RE	PCB	SAM	1	DCB failing low in both column,	CONFIRMS	Surr Fail UPDATE STATUS

		17:37					AR1260 hits		
L3895-08	PO071159.D	03 Sep 2020 17:54	SB-23A-(1.5-2)	PCB	SAM	1	AR1260 Hits, Need 100X, Already run	NotOK	OR Fail UPDATE STATUS
L3895-08DL	PO071160.D	03 Sep 2020 18:12	SB-23A-(1.5-2) DL	PCB	SAM	20	AR1260 Hits, Need more dilution	DILUTION	Surr, OR Fail UPDATE STATUS
L3895-08DL	PO071161.D	03 Sep 2020 18:29	SB-23A-(1.5-2) DL	PCB	SAM	100	AR1260 Hits	OK	Surr Fail UPDATE STATUS
L3896-01	PO071162.D	03 Sep 2020 18:46	WC-1	PCB	SAM	1	Need cleanup	NotOK	UPDATE STATUS
I.BLK	PO071163.D	03 Sep 2020 19:37	I.BLK	PCB	IBLK	1	--	OK	UPDATE STATUS
AR1660CCC500	PO071164.D	03 Sep 2020 19:54	AR1660CCC500	PCB	CCAL500	1	--	OK	UPDATE STATUS
Read Files			Update			All Manual Integration Report			

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