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[Dashboard](#) | [Edd](#) | [Efolder](#) | [Fax](#) | [Genchem](#) | [Projecttrack](#) | [Qaqc](#) | [Quanttrack](#) | [Report](#) | [Sequence](#) | [Utility](#) |

[Home](#) >> [Sequence Module](#) >> [File Info](#)

Sequence Module :: File Info

Navigation Bar

Method : 8082A

[FileInfo Form](#) [Get Test Info](#) [Analysis RunLog](#) [Sequence Info](#) [Maintenance](#) [Audit Summary Report](#) [Sequence Process](#)

Data For Sequence PO110520 CHEMTECH-NJ Lab Sequence Rule For Ms/Msd of EPH

[Sequence Transfer](#)

[Review Raw Data](#)

[Non Conformance Summary](#)

No Review & No Supervised

Reviewed & No Supervised

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SampleID	Data File	Date & Time	ClientId	Test	QCType	Dilution.	Comments	Status	System Comments
PO110520ICV500	PO073093.D	05 Nov 2020 9:17	ICVPO110520	PCB	SAM	1	--	OK	Surr Fail UPDATE STATUS
AR1221ICV500	PO073094.D	05 Nov 2020 9:34	ICVPO110520	PCB	SAM	1	--	OK	Surr Fail UPDATE STATUS
AR1242ICV500	PO073095.D	05 Nov 2020 9:56	ICVPO110520	PCB	SAM	1	--	OK	Surr Fail UPDATE STATUS
AR1248ICV500	PO073096.D	05 Nov 2020 10:31	ICVPO110520	PCB	SAM	1	--	OK	Surr Fail UPDATE STATUS
AR1254ICV500	PO073097.D	05 Nov 2020 10:56	ICVPO110520	PCB	SAM	1	--	OK	Surr Fail UPDATE STATUS
AR1660CCC500	PO073098.D	05 Nov 2020 11:16	AR1660CCC500	PCB	CCAL500	1	--	OK	UPDATE STATUS
I.BLK	PO073099.D	05 Nov 2020 11:33	I.BLK	PCB	IBLK	1	--	OK	UPDATE STATUS
L4573-01RE	PO073100.D	05 Nov 2020 11:50	OB-722-EBRE	PCB	SAM	1	All surrogate failing high	NotOK	Surr Fail UPDATE STATUS
L4573-05MS	PO073101.D	05 Nov 2020 12:24	OB-722MS	PCB	MS	1	MSD is notok	NotOK	Surr Fail UPDATE STATUS
L4573-06MSD	PO073102.D	05 Nov 2020 12:50	OB-722MSD	PCB	MSD	1	DCB high in both column, F flag in AR1016 1st column	NotOK	Surr Fail UPDATE STATUS

L4565-01RE	PO073103.D	05 Nov 2020 13:07	FK-GF-02-063-ARE	PCB	SAM	1	DCB high in both column, confirm status	CONFIRMS	Surr Fail UPDATE STATUS
AR1660CCC500	PO073104.D	05 Nov 2020 13:24	AR1660CCC500	PCB	CCAL500	1	--	OK	UPDATE STATUS
I.BLK	PO073105.D	05 Nov 2020 13:45	I.BLK	PCB	IBLK	1	--	OK	UPDATE STATUS
PB132830BL	PO073106.D	05 Nov 2020 14:02	PB132830BL	PCB	MB	1	--	OK	UPDATE STATUS
PB132830BS	PO073107.D	05 Nov 2020 14:19	PB132830BS	PCB	LCS	1	--	OK	UPDATE STATUS
L4590-01	PO073108.D	05 Nov 2020 14:36	NB-08-110320	PCB	SAM	1	All surrogate failing high	NotOK	Surr Fail UPDATE STATUS
L4624-02	PO073109.D	05 Nov 2020 14:53	COMP-1	PCB	SAM	1	AR1242 Hits, Need 10X, Need to run CCC	NotOK	OR Fail UPDATE STATUS
L4624-05	PO073110.D	05 Nov 2020 15:10	COMP-2	PCB	SAM	1	AR1242 Hits, Need 10X, Need to run CCC	NotOK	OR Fail UPDATE STATUS
L4624-08	PO073111.D	05 Nov 2020 15:27	COMP-3	PCB	SAM	1	AR1242 Hits, Need 10X, Need to run CCC	NotOK	OR Fail UPDATE STATUS
L4625-01	PO073112.D	05 Nov 2020 15:44	CONTAINER-001-A-B-COMP	PCB	SAM	1	AR1268 hits, DCB high in both column	RERUN	Surr Fail UPDATE STATUS
L4625-12	PO073113.D	05 Nov 2020 16:01	CONTAINER-002-A-B-COMP	PCB	SAM	1	AR1260,1268 hits, DCB high in both column, Need to confirm hit	RERUN	Surr Fail UPDATE STATUS
L4625-12MS	PO073114.D	05 Nov 2020 16:18	CONTAINER-002-A-B-COMPMS	PCB	MS	1	F flag in AR1268, Need to confirm hit for original samp	NotOK	Surr Fail UPDATE STATUS
L4625-12MSD	PO073115.D	05 Nov 2020 16:35	CONTAINER-002-A-B-COMPMSD	PCB	MSD	1	F flag in AR1268, Need to confirm hit for original samp	NotOK	Surr Fail UPDATE STATUS
AR1660CCC500	PO073116.D	05 Nov 2020 17:09	AR1660CCC500	PCB	CCAL500	1	--	OK	UPDATE STATUS
I.BLK	PO073117.D	05 Nov 2020 17:27	I.BLK	PCB	IBLK	1	--	OK	UPDATE STATUS
Read Files			Update			All Manual Integration Report			

[Read QUANT File](#)
[EPH Sequence Transfer](#)
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